

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L10	1	(US-5874330-\$).did.	USPAT	OR	OFF	2006/01/31 09:28
L12	463	sidewall with peripheral and memory with sidewall	US-PGPUB; USPAT; EPO; DERWENT; IBM_TDB	OR	ON	2006/01/31 09:33
L13	290	sidewall with peripheral with (circuit circuitry logic driver driving) and memory with sidewall	US-PGPUB; USPAT; EPO; DERWENT; IBM_TDB	OR	ON	2006/01/31 09:36
L14	6	"5672533" and sidewall	US-PGPUB; USPAT; EPO; DERWENT; IBM_TDB	OR	ON	2006/01/31 10:00
L15	30500	(width thickness) with sidewall	US-PGPUB; USPAT; EPO; DERWENT; IBM_TDB	OR	ON	2006/01/31 10:01
L16	4878	(width thickness) with sidewall same gate	US-PGPUB; USPAT; EPO; DERWENT; IBM_TDB	OR	ON	2006/01/31 10:03
L17	1271	16 and memory and (peripheral peripheral logic driver driving)	US-PGPUB; USPAT; EPO; DERWENT; IBM_TDB	OR	ON	2006/01/31 10:03
L18	2184	(width thickness) near3 sidewall same gate	US-PGPUB; USPAT; EPO; DERWENT; IBM_TDB	OR	ON	2006/01/31 11:19
L19	255	18 and memory and (peripheral peripheral)	US-PGPUB; USPAT; EPO; DERWENT; IBM_TDB	OR	ON	2006/01/31 10:03
L20	2	("5091763"   "5173752").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/01/31 10:14
L21	24	(Nakajima-H\$ or Kuroda-H\$).in. and sidewall	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/31 10:15

L22	5	(Nakajima-H\$ or Kuroda-H\$).in. and sidewall with (width thickness)	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/31 10:20
L23	7	"5,397,724" and sidewall with (width thickness)	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/31 10:40
L26	11	22 or 23	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/31 10:20
L27	107	(performance speed) with logic and sidewall adj spacer with (width thickness)	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/31 10:43
L28	8	19 and 27 and @pd>"20000616"	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/31 10:43
L29	1	(US-5021354-\$).did.	USPAT	OR	OFF	2006/01/31 11:19
L30	1	29 and (width thickness) near3 sidewall same gate	US-PGPUB; USPAT; EPO; DERWENT; IBM_TDB	OR	ON	2006/01/31 11:21
L31	2	(10 or 29) and (width thickness) near3 sidewall same gate	US-PGPUB; USPAT; EPO; DERWENT; IBM_TDB	OR	ON	2006/01/31 11:22
L32	560	(wide width) with spacer with gate	US-PGPUB	OR	ON	2006/01/31 12:09
L33	488	gate adj electrode adj length	USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/01/31 12:09
L34	4431	(257/335,336,344,401,408,412,413, 900).CCLS.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/01/31 12:09
L35	208	L34 and (silicon adj nitride SiN) near3 spacer	US-PGPUB; USPAT	OR	OFF	2006/01/31 12:09
L36	4431	(257/335,336,344,401,408,412,413, 900).CCLS.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/01/31 12:09
L37	9576	((257/314,344,371,391,402,404, 408,900) or (438/199,230,231,233, 241,303,305,307,587,595,981)). CCLS.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/01/31 12:09
L38	693	oxide with sidewall spacer and nitride with sidewall spacer	US-PGPUB; USPAT	ADJ	OFF	2006/01/31 12:09
L39	145	oxide with sidewall spacer and nitride with sidewall spacer with (nanometer or angstrom or nm)	US-PGPUB; USPAT	ADJ	ON	2006/01/31 12:09

L40	75	oxide with sidewall spacer with (nanometer or angstrom or nm) and nitride with sidewall spacer with (nanometer or angstrom or nm)	US-PGPUB; USPAT	ADJ	ON	2006/01/31 12:09
L41	3944	((257/344,402,404,408) or (438/230,241,587,595)).CCLS.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/01/31 12:09
L42	146	(wide width) with spacer with gate and @pd>"20050512"	US-PGPUB; USPAT	OR	OFF	2006/01/31 12:09
L43	38	gate adj electrode adj length and @pd>"20050512"	USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/01/31 12:09
L44	46	"5405791" and width	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/31 12:09
L45	9	(US-20040075151-\$ or US-20040262784-\$).did. or (US-5021354-\$ or US-5786247-\$ or US-5874330-\$ or US-6248623-\$ or US-6316304-\$ or US-6344398-\$ or US-6504192-\$ or US-5405791-\$). did.	US-PGPUB; USPAT	OR	OFF	2006/01/31 12:09
L46	1910	(isolation regions or sidewall spacers or dummy gate (ELECTRODE OR ELECTRODES)) and @pd>"20050512"	US-PGPUB; USPAT	ADJ	OFF	2006/01/31 12:09
L47	17831	isolation regions or sidewall spacers or dummy gate (ELECTRODE OR ELECTRODES)	US-PGPUB; USPAT	ADJ	OFF	2006/01/31 12:09
L48	14	isolation regions AND sidewall spacers AND dummy gate (ELECTRODE OR ELECTRODES)	US-PGPUB; USPAT	ADJ	OFF	2006/01/31 12:09
L49	101	isolation regions AND TRANSISTOR ARRAY	US-PGPUB; USPAT	ADJ	OFF	2006/01/31 12:09

# Interferance Text Search

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	4592	(oxide with spacer).clm.	US-PGPUB; USPAT	OR	ON	2006/01/31 12:52
L2	7500	(gate with spacer).clm.	US-PGPUB; USPAT	OR	ON	2006/01/31 12:52
L3	34069	(gate adj electrode).clm.	US-PGPUB; USPAT	OR	ON	2006/01/31 12:53
L4	861266	(width heighth distance thickness). clm.	US-PGPUB; USPAT	OR	ON	2006/01/31 12:53
L5	517	1 with 4	US-PGPUB; USPAT	OR	ON	2006/01/31 12:53
L6	138	5 and 3	US-PGPUB; USPAT	OR	ON	2006/01/31 12:53
L7	2974	2 and 3	US-PGPUB; USPAT	OR	ON	2006/01/31 12:54
L8	125	6 and 7	US-PGPUB; USPAT	OR	ON	2006/01/31 12:54